#### Lecture 9: Synchronization

- . Concurrency examples and the need for synchronization
- definition of mutual exclusion (MX)
- programming solutions for MX
  - MX algorithm for 2 processes (Peterson's algorithm)
  - MX algorithm for multiple processes (bakery algorithm)

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#### "Too much milk" example

Time	You	Your Roommate	
3:00	Arrive home		
3:05	Look in fridge, no milk		
3:10	Leave for grocery		
3:15		Arrive home	
3:20	Arrive at grocery	Look in fridge, no milk	
3:25	Buy milk, leave	Leave for grocery	
3:30			
3:35	Arrive home	Arrive at grocery	
3:36	Put milk in fridge		
3:40		Buy milk, leave	
3:45			
3:50		Arrive home	
3:51		Put milk in fridge	
3:51	Oh, no! Too much milk!!		
The problem here is that the lines:			

Look in fridge, no milk through "Put milk in fridge

are not an atomic operation - an atomic operation cannot be overlapped by another atomic operation

#### "running contest" example

Thread A	Thread B
i = 0	i = 0
while (i < 10)	while (i > -10)
i = i + 1	i = i - 1
print "A wins"	print "B wins"

- Assumptions:

  - Memory load and store are atomic
  - Increment and decrement at not atomic
- Questions:
  - Who wins?
  - Is it guaranteed that someone wins?
  - · What if both threads have their own CPU, running concurrently at
  - exactly the same speed? Is it guaranteed that it goes on forever? What if they are sharing a CPU?

# Mutual exclusion (MX)

- . To avoid race conditions mutual exclusion is used:
- Mutual exclusion (MX) ensures that only one thread does a particular . activity at a time - all other threads are excluded from doing that activity
- Critical section (CS)- code that only one thread can execute at a time (e.g., code that modifies shared data)
- threads alternate between executing CS and non-CS. A thread may execute non-CS code indefinitely but it spends only finite amount of time within CS
- Solution must be fair, that is:
  - Avoid starvation if a thread starts trying to gain access to the critical section, then it should eventually succeed
- Methods to enforce mutual exclusion
  - Up to user threads have to explicitly coordinate with each other Up to OS — OS provides support for mutual exclusion
  - Up to hardware hardware provides architectural support for mutual exclusion

### Synchronization problem of concurrency

- Concurrency simultaneous execution of multiple threads of control
- concurrency appears in:
  - · mutiprogramming management of multiple processes on a uniprocessor architecture
  - mutliprocessing management of multiple processes on a multiprocessor architecture
  - · distributed computing management of multiple processes on multiple independent computers
- Synchronization using atomic (indivisible) operations to ensure cooperation between threads
- · race condition the outcome of the multiple process execution depends on the order of the execution of the instructions of the processes

## MX algorithm 1

- t1 ( ) { while (true) { ; /\* do nothing \*/ CS while (turn != 1) turn = 2;non-CS } } t2 ( ) { while (true) { ; /\* do nothing \*/ while (turn != 2) CS turn = 1; non-CS } }
- Threads take
  - turns entering CS.
  - + t1 checks if it's her turn. It it is not (turn!=1) then it waits doing nothing

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- if it is t1's turn it proceeds with it's CS and setting turn to 2 giving t 2 an
- opportunity to enter her CS advantages:

enforces MX

• processes must alternate in CS requests, what if they are executing with different speeds? What if t2 does not want to get into CS for a while? 6

#### MX algorithm 2a

}

}

```
    Before entering CS thread

                                               checks if the other is in CS
                                                t1_in_CS,t2_in_CS
                                                  indicate if corresponding
         CS
                                                  thread is in CS
         t1_in_CS = false;

    advantage - fair

         non-CS
    }
                                           problem - no MX!
}
                                              After t1 decides that t2 is
                                           .
                                               not in CS it is already in CS.
t2 ( ) {
   while (true) {
    while (t1_in_CS == true)
        ; /* do nothing */
        t2_in_CS = true;
                                               Yet it takes time for t1 to set
                                               its own flag t1_in_Cs to true
         CS
         t2 in CS = false;
        non-CS
    }
```

## MX algorithm 2b

```
;
CS
             /* do nothing */
      t1_in_CS = false;
      non-CS
   }
}
,
t2(){
   while (true) {
    t2_in_CS = true;
      while (t1_in_CS == true)
     _e
;
cs
+
               /* do nothing */
      t2_in_CS = false;
      non-CS
  }
}
```

- Let's move the setting of the t1\_in\_Cs flag outside of the neighbor's flag checking
   advantage MX
- problem not fair!
  - Both threads cannot get to CS if they set their flags at the same time!
  - This condition is called deadlock and we say that threads may starve

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## MX algorithm 3 (Peterson's alg.)

turn = 2; while (t2\_in\_CS == true && turn == 2) ; /\* do nothing \*/ cs non-CS { (true) { { t2\_in\_CS = true; t2\_in\_CS = true && title blocked on its while (turn == 1): t2\_in\_CS = true && title blocked on its while (turn == 2) Peterson's alg. enforces MX Peterson's alg. avoids deadlock: tarter to tarter t

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```
t1 cannot be blocked when:
t2 is in non-cs:
t2_in_cs == false
```

t2 is in while: turn == 1

int i; /\* unique process id \*/

while (true) {
 choosing[i]=true;

}

Multiple Process MX (bakery alg.)

Peterson's alg. for n processes.
Each process has unique identifier
the process takes a

Bakery alg. is a

generalization of

```
    the process takes a
number when CS is
needed; in case of a tie
process ids are used
```

```
    the process with the 
lowest number is allowed 
to proceed
```

```
    why does it check that
choosing is not set when
trying to access CS<sup>2</sup><sub>10</sub>
```